

IN THE CLAIMS:

Please amend claim 2 as follows:

1. (Cancelled)

2. (Currently Amended) A display device comprising:

a display unit which displays an image;

a display-data line which supplies data of the image from an exterior to said display unit;

memories which store information for controlling displaying of the data of the image on said display unit, said information being different from said data of the image;

an operation circuit unit which controls said display unit to display the data of the image supplied through said display-data line based on the information stored in said memories;

a data bus which connects each one of said memories to an exterior of said display device, and supplies the information to said memories from the exterior of said display device; and

an address bus which connects said each one of said memories to the exterior of said display device, and supplies address signals for selecting one of said memories such that the information is written to the one of said memories selected by the address signals,

wherein said operation circuit unit includes:

a gate driver which drives gate lines of said display unit; and

a data driver which drives data lines of said display unit, wherein at least one of said gate driver and said data driver operates based on the information sorted in said memories.

3. (Original) The display device as claimed in claim 2, wherein the at least one of said gate driver and said data driver includes a shift register which operates based on the information stored in said memories to control a scan direction of said display unit.

4. (Original) The display device as claimed in claim 2, wherein the at least one of said gate driver and said data driver includes a decoder which operates based on the information stored in said memories to control a scan direction and a scan order of said display unit.

5. (Original) The display device as claimed in claim 4, wherein the at least one of said gate driver and said data driver further includes an address counter which operates based on the information stored in said memories to supply an address to said decoder, said decoder decoding the address to control the scan direction and the scan order of said display unit.

6. (Original) The display device as claimed in claim 2, wherein said memories store pattern data, said data driver operating in accordance with the pattern data stored in said memories to control said display unit to display an image corresponding to the pattern data.

7. (Original) The display device as claimed in claim 6, wherein said operation circuit unit further includes a data-synthesis circuit which combines the pattern data stored in said memories and display data supplied from the exterior of said display device to generate synthesized pattern data, said data driver operating in accordance with the synthesized pattern data to control said display unit to display an image corresponding to the synthesized pattern data.

8. (Previously Presented) The display device as claimed in claim 2, further comprising:

a display information acquisition circuit which acquires information about said display unit; and

display-information memories which store the information about said display unit, and are connected to said data bus and said address bus so as to supply the information about said display unit to the exterior of said display device when so requested.

9. (Original) The display device as claimed in claim 8, wherein said display-information acquisition circuit checks said display unit to acquire the information about the said display unit with regard to a defect of said display unit.

10. (Original) The display device as claimed in claim 8, wherein said display-information acquisition circuit acquires the information about the said display unit with regard to coordinates of a position at which input is entered on said display unit.

11. (Original) The display device as claimed in claim 2, wherein said display unit includes:

a plurality of polysilicon thin-film transistors; and

a plurality of pixel electrodes corresponding to the respective polysilicon thin-film transistors, wherein display data is supplied to the pixel electrodes via the polysilicon thin-film transistors selected by said gate driver and said data driver.